

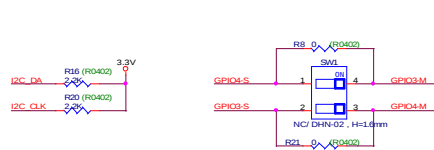
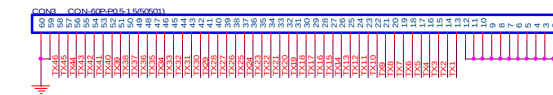
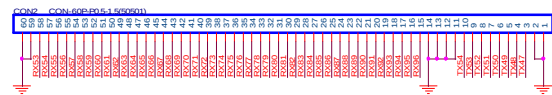
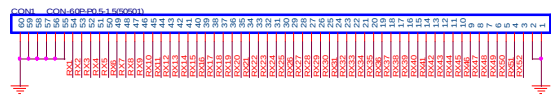
Revision	Item	History	Date
REV:0.1	1	Initial	2014.12.29

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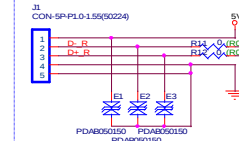
Title
History

Size A4	Document Number SiS9250Tx2+SiS9202_Lenovo_C51_23" _V01	Rev V0.1
Date:	Tuesday, December 30, 2014	Sheet 1 of 2



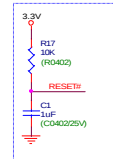
<Golden Finger> Digital Interface

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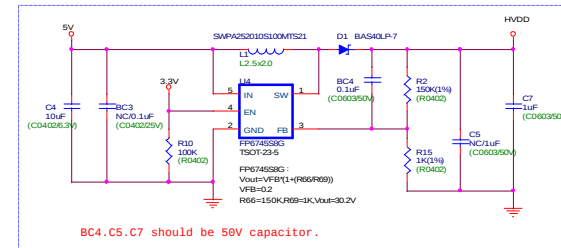


E1-E3 place near J1.
Reserve E1-E3(TVS diode) for ESD consideration.

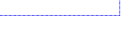
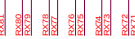
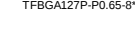
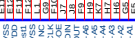
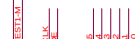
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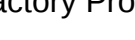
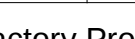
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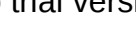
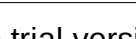
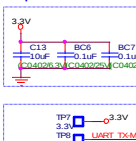
BC4.C5.C7 should be 50V capacitor.



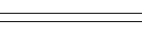
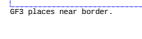
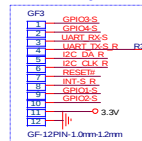
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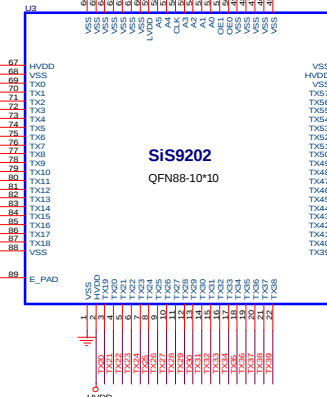
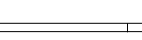
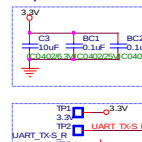
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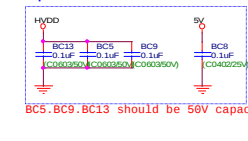
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<Capacitor for Power PIN>



<Capacitor for Power PIN>



BC5.BC9.BC13 should be 50V capacitor.

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File	SiS9250Tx2+SiS9202		
Size	Document Number	Document Number	Rev
A2	SiS9250Tx2+SiS9202_Lenovo_C51_23_V01	SiS9250Tx2+SiS9202_Lenovo_C51_23_V01	V0.1
Date	Tuesday, December 30, 2014	Sheet	2 of 2